Subject Invitation to contribute to the book: VLSI-SoC: EDA the New Technology Enabler

To Me <matteo.grimaldi@polito.it>☆

Dear Matteo Grimaldi,

we are glad to let you know that your paper entitled:

Arbitrary-Precision Convolutional Neural Networks on Low-Power IoT Processors

presented at the 27th IFIP/IEEE International Conference on Very Large Scale Integration (VLSI-SoC) held in Cusco, on October 6-9, 2019 has been selected for publication in the post-conference book "VLSI-SoC: EDA the New Technology Enabler".

This book will be published by Springer, and it contains a selection of the best contributions to the Conference. The book will contain approximately 18 chapters. This is a great achievement if you consider that the TPC selected 18 full papers out of 105 submissions; your paper further distinguished itself as standing in the top of this selected group. The selection process took into account the scores and comments received in the review process and the review forms filled out by the TPC members and session chairs after the presentations.

The series of books published by Springer from the selection of best contributions to the previous VLSI-SoC display some of the most advanced research in the field of VLSI. As an example, you can see here the most recently published book:

https://www.springer.com/qp/book/9783319671031

Springer commercializes these books under a contract with the Conference sponsor IFIP (International Federation on Information Processing, http://www.ifip.org), a non-governmental, non-profit umbrella organization for national societies working in the fields of information and communications technologies and sciences, established in 1960 under the auspices of UNESCO. IFIP sponsors VLSI-SoC by offering travel grants to select PhD students that participate in the Conference from emerging countries, receiving in return royalties from the publication of the book. We, the book editors, are of course volunteers with no profit.

We invite you to prepare an extended version of your paper to be published as a chapter in this book. A book chapter should have at least 30% additional content with respect to your paper in the Proceedings of VLSI-SoC 2019. The major deadlines for book preparation are the following: The book chapter must do a reference to the version of the paper published in the VLSI-SoC proceedings with IEEE. The title of the book chapter must be different from the published conference paper (minor changes are accepted).

- Jan 20, 2020 acceptance or rejection of the invitation (please email the book editors, found at the end of this email, and do not reply to this email; it will not be monitored)
- Feb 27, 2020: complete version of the book chapter sent to the Editors
- Mar 16, 2020: comments/corrections from the book Editors sent to the Authors
- Mar 30, 2020: Deadline for final version after taking care of editor feedback
- April 20, 2020: book sent to Springer for publication.

We are looking forward to hearing your response about your contribution to this book. If you accept this invitation, we will send you the material for compilation of your book chapter (template and copyright form). Please reply as soon as possible and no later than by January 20.

Kind regards,

The Book Editors

Carolina Metzler, Pierre-Emmanuel Gaillardon Giovanni De Micheli Carlos Silva Cardenas Ricardo Reis